

Patent 7,739,316

PATENT

IN UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: J. Patrick Thompson et al.

Examiner: LY, CHEYNE D

Patent No.: 7,739,316

Group Art Unit: 2168

Issue Date: June 15, 2010

Confirmation No.: 1753

Customer No.: **22971**

Docket No: 302726.01

Title: SYSTEMS AND METHODS FOR THE IMPLEMENTATION OF A BASE SCHEMA
FOR ORGANIZING UNITS OF INFORMATION MANAGEABLE BY A
HARDWARE/SOFTWARE INTERFACE SYSTEM

REQUEST FOR CERTIFICATION OF CORRECTION

Attn: Certificate of Correction Branch
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

It is requested that a Certificate of Correction be issued correcting printing errors appearing in the above-identified United States patent. Two copies of the text of the Certificate in the suggested form are enclosed.

Issuance of the Certificate of Correction would neither expand nor contract the scope of the claims as properly allowed, and re-examination is not required.

As the error is that of the Patent Office, it is believed that no fee is due.

Respectfully submitted,

Customer Number: 22971

Date 1/4/2011



Christine Hartness

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

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We are transmitting herewith the attached:

- ☒ Request for Certificate of Correction (1 pg.).
- ☒ Certificate of Correction Form - PTO-1050 (1 pg.)

Customer Number: 22971


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Christine Hartness

**UNITED STATES PATENT AND TRADEMARK OFFICE
CERTIFICATE OF CORRECTION**

PATENT NO: 7,739,316

DATED: June 15, 2010

INVENTOR(S): J. Patrick Thompson et al.

It is certified that errors appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

In Face page, in field (54), under "Title" column 1, line 2, after "OF" insert -- A --.

In column 1, line 2, after "OF" insert -- A --.

In column 116, line 41, below "logic circuits." insert -- APPENDIX A --.

In column 125, line 1, (APPENDIX B), after "using" insert -- in --.

MAILING ADDRESS OF SENDER:

CPA Global
P.O. Box 52050
Minneapolis, MN 55402

Atty Docket No: 302726.01

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